

**5-Year UNDERGRADUATE CURRICULUM PLAN**  
**Bachelor of Science in Electrical Engineering**  
**(DSP Elective Sequence)**  
**Effective 2003-2004 Catalog Year**

**FALL SEMESTER**

**SPRING SEMESTER**

<u>Course</u>	<u>Title</u>	<u>Units</u>		<u>Course</u>	<u>Title</u>	<u>Units</u>
<b>1<sup>st</sup> Year</b>						
UNIV 100	University 100	1		MATH 123	Calculus II	4
MATH 122	Calculus I (GE – B2)	4		PHYS 151	Mechanics and Heat (GE-B1b)	4
CECS 174	Programming & Problem Solving I	3		EE 200	Trends in EE	1
	<b>G.E. (Category A1,A2)</b>	<b>6</b>			<b>G.E. (Category A3)</b>	<b>3</b>
	<b>TOTAL</b>	<b>14</b>			<b>TOTAL</b>	<b>12</b>
<b>2<sup>nd</sup> Year</b>						
MATH 224	Calculus III	4		PHYS 152 OR EE 210 & EE 210L		4/4
EE 201	Digital Logic Design	4		ENGR 202	Computer Methods in Engineering	3
	<b>GE</b>	<b>6</b>			<b>GE</b>	<b>6</b>
	<b>TOTAL</b>	<b>14</b>			<b>TOTAL</b>	<b>13</b>
<b>3<sup>rd</sup> Year</b>						
EE 211	Electric Circuits I	3		Math 370A	Applied Mathematics I	3
EE 211L	Electric Circuits Lab	1		EE 310	Electric Circuits II	3
EE 346	Microprocessor Princ. & Appl.	3		PHYS 254	Applied Modern Physics	3
EE 380	Engineering Probability 7 Stat.	3		EE 347	Microprocessor Based System Des.	3
	<b>GE</b>	<b>3</b>			<b>GE</b>	<b>3</b>
		13				
	<b>TOTAL</b>	<b>15</b>				
<b>4<sup>th</sup> Year</b>						
EE 330	Analog Electronics Circuits I	4		EE 382	Communication System I	3
EE 350	Energy Conversion Principles	3		EE 386	Digital Signal Processing I	3
CE 370/ MAE 330	Anal. Mech./ Eng. Thermo. I	3		EE 370	Control Systems	3
	<b>G.E.</b>	<b>3</b>		EE 430	Analog Electronic Circuits I	3
	<b>TOTAL</b>	<b>13</b>		EE 430L	Engineering Electronics II Lab	1
		13				
	<b>TOTAL</b>	<b>13</b>				
<b>5<sup>th</sup> Year</b>						
EE 400D	EE Sem. & Project Design Lab	2		EE 370L	Control Systems Lab	1
EE 482	Communication Systems II	3		EE 462 OR 464	E&M Wireless/ E&M Optics	3
EE 486	Digital Signal Processing	3		EE 489*	Digital Signal Processing Design	3
EE ---	DSP A.E.	3		EE ---	DSP A.E.	3
	<b>G.E.</b>	<b>3</b>			<b>G.E.</b>	<b>3</b>
	<b>TOTAL</b>	<b>14</b>			<b>TOTAL</b>	<b>13</b>

\*Senior Design Project – 2 hours lecture, 3 hour lab

A.E. Approved DSP Electives to minimum of 134 units. See advisor for selection of approved elective courses. Note: Other 400 level elective courses must be approved by the DSP Elective Sequence Advisor. 500 level elective courses require approval of both the Sequence Advisor and the Associate Dean of Instruction.

TOTAL DSP UNITS

134