

**5-Year UNDERGRADUATE CURRICULUM PLAN**  
**Bachelor of Science in Electrical Engineering**  
**(ANALOG and DIGITAL ELECTRONICS Elective Sequence)**  
**Effective 2003-2004 Catalog Year**

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**FALL SEMESTER**

**SPRING SEMESTER**

<u>Course</u>	<u>Title</u>	<u>Units</u>
UNIV 100	<b>University 100</b>	<b>1</b>
MATH 122	<b>Calculus I (GE – B2)</b>	<b>4</b>
CECS 174	Programming & Problem Solving I	3
	<b>G.E. (Category A1,A2)</b>	<b><u>6</u></b>
	<b>TOTAL</b>	<b>14</b>

<u>Course</u>	<u>Title</u>	<u>Units</u>
MATH 123	Calculus II	4
PHYS 151	<b>Mechanics and Heat (GE-B1b)</b>	<b>4</b>
EE 200	Trends in EE	1
	<b>G.E. (Category A3)</b>	<b><u>3</u></b>
	<b>TOTAL</b>	<b>12</b>

**1<sup>st</sup> Year**

MATH 224	Calculus III	4
EE 201	Digital Logic Design	4
	<b>GE</b>	<b><u>6</u></b>
<b>TOTAL</b>		<b>14</b>

PHYS 152 OR EE 210 & EE 210L		4/4
ENGR 202	Computer Methods in Engineering	3
EE 380	Engineering Probability 7 Stat.	3
	<b>GE</b>	<b><u>3</u></b>
	<b>TOTAL</b>	<b>13</b>

**2<sup>nd</sup> Year**

EE 211	Electric Circuits I	3
EE 211L	Electric Circuits Lab	1
EE 346	Microprocessor Princ. & Appl.	3
PHYS 254	Applied Modern Physics	3
	<b>GE</b>	<b><u>3</u></b>
	<b>TOTAL</b>	<b>13</b>

Math 370A	Applied Mathematics I	3
EE 310	Electric Circuits II	3
EE 320	<i>Solid State Electronic Devices</i>	3
EE 347	Microprocessor Based System Des.	3
	<b>GE</b>	<b><u>3</u></b>
	<b>TOTAL</b>	<b>15</b>

**3<sup>rd</sup> Year**

EE 330	Analog Electronics Circuits I	4
EE 350	Energy Conversion Principles	3
CE 370/ MAE 330	Anal. Mech./ Eng. Thermo. I	3
	<b>G.E.</b>	<b><u>3</u></b>
	<b>TOTAL</b>	<b>13</b>
	<b>TOTAL</b>	<b>13</b>

EE 382	Communication System I	3
EE 386	Digital Signal Processing I	3
EE 370	Control Systems	3
EE 430	Analog Electronic Circuits I	3
EE 430L	Engineering Electronics II Lab	<u>1</u>
	<b>TOTAL</b>	<b>13</b>

**4<sup>th</sup> Year**

EE 400D	EE Sem. & Project Design Lab	2
EE 332	<i>Digital Elect. Circuits</i>	3
EE 435*	<i>Microelectronics</i>	3
EE 435L*	<i>Microelectronics Laboratory</i>	1
EE ---	<i>Analog/Digital A.E</i>	3
	<b>G.E.</b>	<b><u>6 or 3</u></b>
	<b>TOTAL</b>	<b>14 or 15</b>

EE 370L	Control Systems Lab	1
EE 462 OR 464	E&M Wireless/ E&M Optics	3
EE 301*	<i>Sequential Circuit Design</i>	3
EE 301L*	<i>Sequential Circuit Design Lab</i>	1
EE 447**	<i>Design of Digital System</i>	3
	<b>G.E.</b>	<b><u>3 or 6</u></b>
	<b>TOTAL</b>	<b>14 or 13</b>

**5<sup>th</sup> Year**

\*Either (EE 301 & 301L) or (EE 435 & 435L) is required.

\*\*Senior Design Project – 2 hours lecture, 3 hour lab

A.E. Approved Analog/Digital Electives to minimum of 135 units. See advisor for selection of approved elective courses. Note: Other 400 level elective courses must be approved by the Analog/Digital Elective Sequence Advisor. 500 level elective courses require approval of both the Sequence Advisor and the Associate Dean of Instruction.

**TOTAL Analog/Digital UNITS**                      135